#### Computational Physics - PH 354

#### Manish Jain

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#### Logistics

- TA: Indrajit Maiti (indrajit@iisc.ac.in)
- Please send an email at this address: ph354.cp@gmail.com to register for getting homeworks via email.
- Course website: http://www.physics.iisc.ernet.in/mjain/pages/teaching.html
- Homeworks will also be posted there periodically. Please check regularly.
- 1st Homework already posted.

#### Grading scheme

- No Exams!!!!!
- Homeworks and projects based (50% and 50% tentatively)
- Project will be decided by you in consultation with your Masters/PhD/Bachelors advisor – subject to my approval as well.
- Stand alone project which you have to get working and submit a working code and report. Also, make a presentation on it at the end of the course.
- Projects can start right away.

#### Basics of Computational Physics

- What is Computational Physics?
- Basic Computer Hardware
- Software: Programming Languages

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"The purpose of computing is insight, not numbers."

— Richard Hamming

What is Computational Physics?

Computational physics is a tool for solving complex numerical problems in physics.

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- Computational Physics does not study computers.

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Many, if not most, problems in physics could never be solved without computers!

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- Symbolic manipulation: Computer Algebra Systems. Examples: Mathematica, SymPy etc.

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#### Computer Hardware

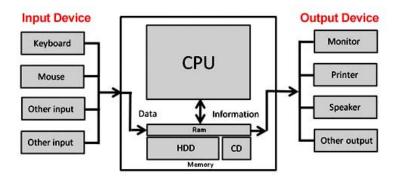
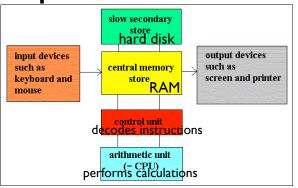


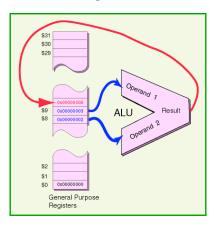
Image:http://tipsboss.com/basic-computer-tips-for-beginners/

## Computer Architecture



both instructions & data sent by input devices to memory loaded from memory to CPU registers
Instruction Set Architecture (ISA): machine language instruction set, word size, registers

#### **ALU**



bitwise logic ops. AND, OR, NOT,XOR

integer arithmetic ops. add, subtract, multiply, divide

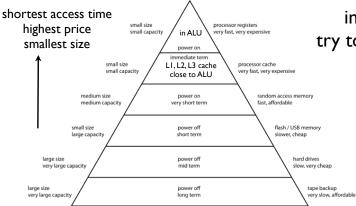
bit shifting (\* or / by  $2^n$ )

#### FPU: floating point unit

+,-,\* fast / slow and so are exp, cos, & other transcendental fns. commonly used function are coded in machine language

# Hierarchical Memory

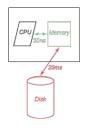
#### Computer Memory Hierarchy



implication: try to reuse cache

## Cache Utilization

#### data stored in memory as a I-D array



column major in fortran

$$\begin{bmatrix} 1 & 2 & 3 \\ 6 & 5 & 4 \end{bmatrix} = \begin{bmatrix} 1 & 6 & 2 & 5 & 3 & 4 \end{bmatrix}$$

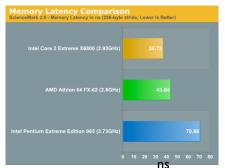
row major in C!

sometimes compilers do these optimizations (-O3)

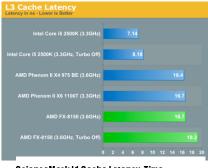
# Latency & Bandwidth

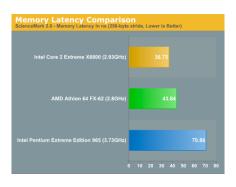
minimum time to do an action (access time)

rate of action once action is initialized

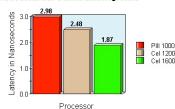








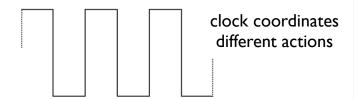
#### ScienceMark L1 Cache Latency: Time



Nehalem processor: LI~64 kB L2~2 MB L3~30 MB

L1 cache ~ 5 times faster than L3 cache ~5 times faster than RAM!

## Clock Rate

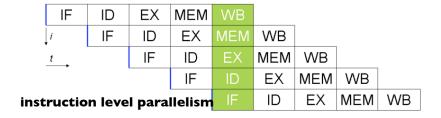


modern CPUs upto 4 FLOPs per cycle: 2.4 GHz =>  $4\times2.4\ 10^9 \sim 10^{10}\ FLOPs/cycle/core$  (10 GF) if the cluster has 80 cores => 800 GF machine

this is not the only parameter! since data access is more time-consuming (40 ns) than FLOPs (0.1 ns); having larger RAM/cache/interconnects more important than just clock speed

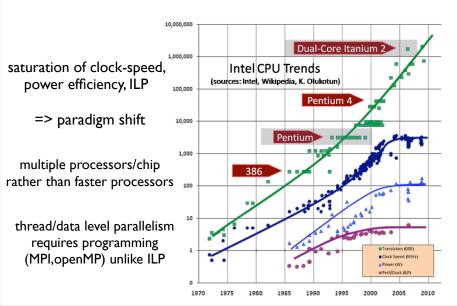
## Architecture level Parallelism

**bit level parallelism:** 4 bit ... 32 to 64 bit word-size (=register size); more bits processed/cycle



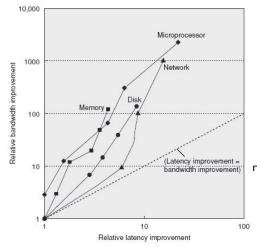
five-stage pipeline in a RISC (IF = Instruction Fetch, ID = Instruction Decode, EX = Execute, MEM = Memory access, WB = Register write back)

# Moore's Law



## improvements governed by technology

#### architecture, compiler, programs reflect this



power issues! chips becoming smaller and smaller

$$P=1/2 C V^2 f$$

higher frequency => more power consumption & heating can't be air cooled! reduce operating voltage (transistor errors), frequency (speed reduction)

## software closely tied to hardware

esp. with parallel systems

source code: high level language (fortran, c, c++)

compiler (also optimizes the code, e.g., -O2, -O3 flags)

object code & executable (lower level assembly/machine code)

interpreted languages (e.g., python, perl,MATLAB, Mathematica, IDL scripting languages) slower but handy/easier

important to remember architecture to attain maximum performance

Programming Languages

The basic ideas behind computational physics are language independent!

■ Choice depends on the problem:

- Choice depends on the problem:
  - Numerical simulation

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- Experience.

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- Practice! Practice!! Practice!!!